

Program:
Sunday February 13th

****Breakfast from 7:30-8:30**

Hosts:
Olaf Lubeck – Los Alamos National Laboratory (Morning Sessions Chair)
Phil Mucci – Univeristy of Tennessee
Rob Fowler – Rice University
Mike Lang – Los Alamos National Laboratory (Afternoon Sessions Chair)

8:30 Welcome(10 mins)
Olaf Lubeck – Los Alamos National Laboratory (Morning Sessions Chair)

8:40 – 10:15 User Space
“*Performance Hardware – If I ran the world*” (15 mins)
Rob Fowler – Rice University

“*What we need to be able to count to tune programs*” (15 Mins)
Mustafa M. Tikir, Bryan R. Buck, Jeffery K. Hollingsworth – University of Maryland

“*Efficient Collection of Information on the Locality of Accesses*” (15 mins)
Irina Chihaia, Thomas Gross – ETH Zurich

“*Hardware Performance Counters for Detailed Runtime Power and Thermal Estimations: Experiences and Proposals*” (15 mins)
Canturk Isci, Gilberto Contreras, Margaret Martonosi – Princeton University

User Space Discussion 1 (35 minutes)

****Morning break 10:15-10:30**

10:30- 12:00 User Space (cont.)
“*System Wide Performance Monitors*”
Jean-Francois Collard, Norm Jouppi – Hewlett-Packard Laboratories

“*Performance Monitoring Hardware will Always be A Low-Priority, Second-Class Feature in Processor Designs Until ...*” (15 mins)
Brinkley Sprunt – Bucknell University

“*Using Hardware Event Counters for Continuous, Online System Optimization: Lessons and Challenges*” (15 mins)
Christos D. Antonopoulos, Dimitrios S. Nikolopoulos

“*A Vision for Next Generation System Monitoring*” (15 mins)
Martin Schulz – Lawrence Livermore National Laboratory, Brian White, Sally A. McKee – Cornell, Hsien-Hsin Lee – Georgia Tech.

User Space Discussion 2 (30 mins)

****Lunch 12-1**

1:00-2:35 Architecture Space

“*Sun’s Perspective on Hardware Performance Monitoring*”
Marty Itzkowitz – Sun (35 mins)

“*The NUMA Challenge*”
Bill Brantley – AMD (25 mins)

“*Experiences with Perfmon*”
Stephane Eranian – HP (15 mins)

Architecture Space Discussion 1 (30 mins)

****Afternoon break 2:45-3:00**

3:00-4:55 Architecture Space (cont.)
“*Confessions of a Performance Monitor Hardware Designer*”
Jim Callister – HP/Intel (25 mins)

“*The Power-PC Performance Monitor*”
Alex Mercias – IBM (25 mins)

“*Intel*”
Nidhi Nidhi – Intel (25 mins)

****Architecture Space Discussion 2 (30 mins)**

“*Summary and wrap up*”
Phil Mucci (15 Mins)

6:30 Reception